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STATEMENT BY APPLICANT**

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**Complete if Known**

Application Number	10/774,857
Filing Date	February 9, 2004
First Named Inventor	Ryan Fung
Art Unit	2825
Examiner Name	Tat, Binh C
Attorney Docket Number	ALT.P027.1 (A1182.1)

Sheet 1 of 2

**U. S. PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
RT		US- 6,826,773	11-2004	Hathaway et al.	
RA		US- 6,763,506	07-2004	Betz et al.	
BS		US- 6,708,139	03-2004	Rearick et al.	
BO		US- 5,659,484	08-1997	Bennett et al.	
BO		US- 5,541,849	07-1996	Rostoker et al.	
BO		US- 5,237,514	08-1993	Curtin, James J.	
BO		US- 5,521,837	05-1996	Frankle et al.	
BO		US- 2005/0251775	11-2005	Wood, John	
BO		US- 2005/0138578	06-2005	Alpert et al.	
BS		US- 2005/0132318	06-2005	Kidd et al.	
BS		US- 2005/0039156	02-2005	Cathoor et al.	
BS		US- 2004/0243964	12-2004	McElvain et al.	
BS		US- 2003/0005398	01-2003	Cho et al.	
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**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> * Number * Kind Code <sup>5</sup> (if known)				

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05/02/06

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Sheet 2	of 2	Attorney Docket Number	ALT.P027.1 (A1182.1)

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
BT		NN9107410, "Linear Algorithm for Distributing Slack So That All Nets of a Design Have a Zero Slack Pin", IBM Technical Disclosure Bulletin, Vol. 34, No. 2, July 1991, pp. 410-414.	
BT		Mathur et al., "Timing-Driven Placement for Regular Architectures", IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, Vol. 16, No. 6, June 1997, pp. 597-608.	

Examiner Signature	<i>Binh C. Tat</i>	Date Considered	08/02/06
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